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Information Disclosure Statement by Applicant

(Use several sheets if necessary)

(37 CFR §1.98(b))

next communication to applicant.

Applicant
Eitan Rosen
Filing Date

July 30, 2003

 $\begin{array}{c} \text{Group Art Unit} \\ 2116 \end{array}$

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	A 1	2005/0015560	1/2005	Bae			
	A2	2006/0017480	1/2006	Lin			
	A3	5,418,822	5/1995	Schlachter et al.			
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Other Documents (include Author, Title, Date, and Place of Publication)					
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	A15	Rabaey et al., "Digital Integrated Circuits: a design perspective", 2003, Pearson Education, 2 nd ed. pp. 453-459.			
	A16	Marvell International Ltd.; Discovery LT MIPS System Controller, MV64420; 2004, 2 pages.			
	A17	Micron Technology, Inc.; TN-46-05 General DDR SDRAM Functionality, Technical Note; July 2001; 11 pages.			
	A18	Lattice Semiconductor Corporation; Dedicated DDR Memory Interface Circuitry, Mar. 17, 2005; 2 pages.			

Examiner Signature /Tse Chen/	Date Considered 11/04/2009				
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